

Abstract of the Disclosure:

An apparatus for producing a clock output signal, having an input for receiving an input signal containing a phase information item; a clock generator for producing a  
5 multiplicity of clock signals whose phases are respectively shifted from one another by a predetermined amount; and a weighting and mixing circuit for weighting each of the multiplicity of clock signals based on the phase information item contained in the input signal. The weighting and mixing circuit is also for mixing the weighted clock signals in order to produce a clock output signal whose phase essentially matches the phase represented by the phase information item.

MPW/cp